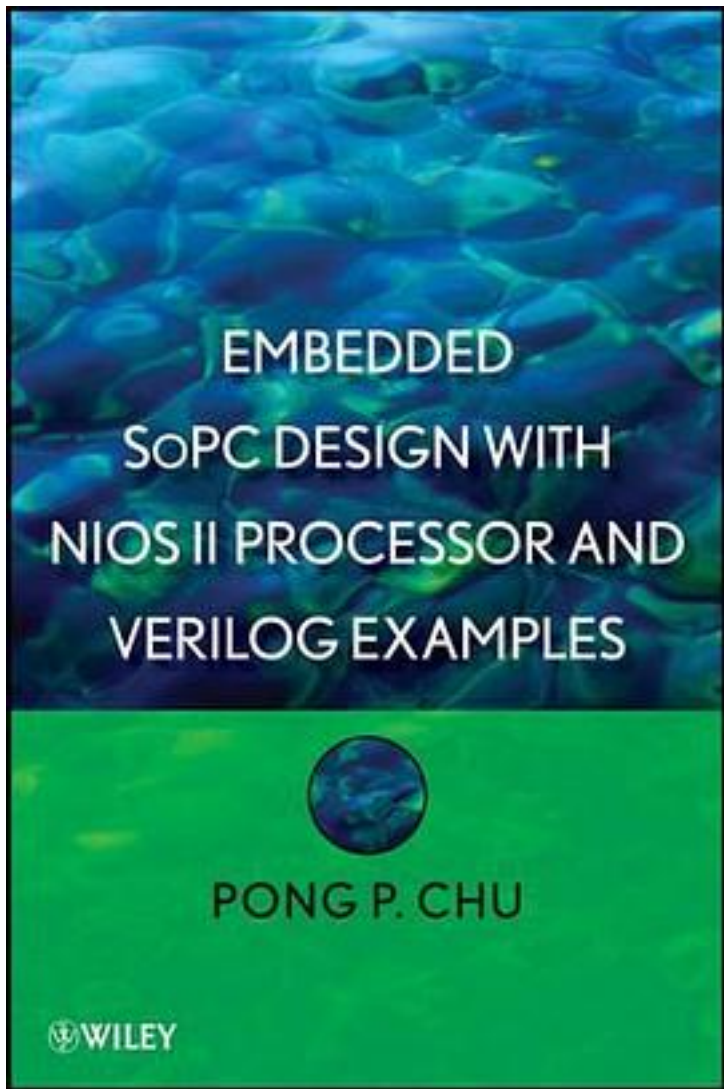# Embedded SoPC Design with Nios II Processor and Verilog Examples

[Embedded SoPC Design with Nios II Processor and Verilog Examples_下载链接1_](#)

著者:Pong P. Chu

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This book explores the unique hardware programmability of FPGA (field-programmable gate array)-based embedded systems, using a learning-by-doing approach to introduce the concepts and techniques for embedded SoPC (system on a programmable chip) systems with Verilog. The book contains a large number of practical examples to illustrate and reinforce the hardware and software design concepts and techniques, as well as a complete code listing and experiment problems. The book is designed for upper-level undergraduate and entry-level graduate students in computer engineering, as well as practicing engineers.

作者介绍:

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