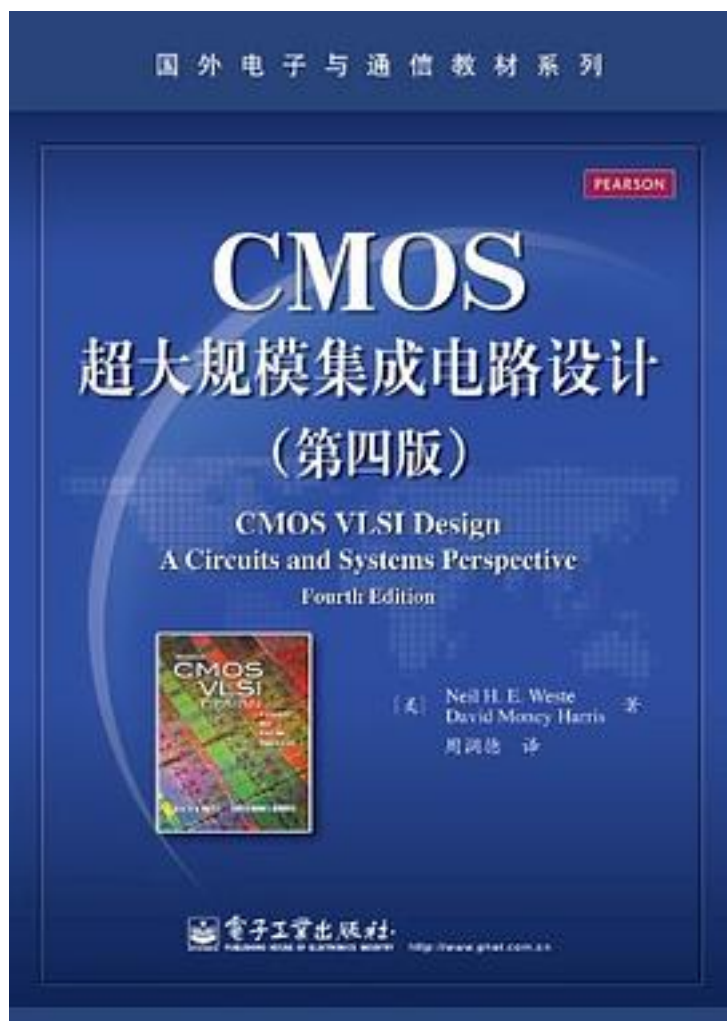


CMOS超大规模集成电路设计



[CMOS超大规模集成电路设计_下载链接1](#)

著者:[美]Neil H. E. Weste

出版者:中国电力出版社

出版时间:2006-4

装帧:

isbn:9787508338620

这本关于CMOS

VLSI设计的畅销图书经过全面修订，囊括了在芯片上设计复杂与高性能CMOS系统的大量先进技术。本书覆盖从数字系统一级到电路一级的CMOS设计方法，其中既介绍了一些基本原理，也介绍了许多很好的设计经验。

Neil H.E.Weste和新的合著者David

Harris利用他们丰富的业界实践经验与教学经验对先进的芯片设计方法进行了介绍。本书作者曾在Intel、Cisco和Bell实验室工作过，作者通过共享他们在这专业领域的经验，从实用的角度阐述了有关CMOS VLSI设计的内容。

作者介绍:

David Money Harris Associate Professor of Engineering at Harvey Mudd College in Claremont, CA, holds a Ph.D. from Stanford University and S.B. and M.Eng. degrees from MIT. His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has designed chips at Sun Microsystems, Intel, Hewlett-Packard, and Evans & Sutherland.

Neil Weste is a member of the faculty at the Department of Electronic Engineering, Macquarie University; Adjunct Professor of Electrical Engineering at The University of Adelaide; and Director, Engineering at Cisco's Wireless Networking Business Unit. He is a Fellow of the IEEE for his contributions to custom IC design, and a peer elected member of the IEEE Solid State Circuits Society. In 1997 he cofounded Radiata Communications (with David Skellern) which designed the first chip sets for the IEEE 802.11a WLAN standard; in 2001 Radiata was acquired by Cisco. He has served as department head at Bell Laboratories; leader of design projects for Symbolics, Inc.; and as president of TLW, Inc., an IC engineering company that completed groundbreaking chip designs for companies such as North American Philips, Analog Devices, AT&T Microelectronics and Thomson Consumer Electronics.

目录:

[CMOS超大规模集成电路设计 下载链接1](#)

标签

VLSI

集成电路

专业书

IC

碉堡了！！！！

微电子

教材

vlsi

评论

太牛逼了,这本才是我大学期间真正没看懂的那本,真的是没看懂啊

也不是看完了。。。但一直放在在读里又没有读真是揪心啊

上vlsi的推荐书目sechen用的哦~~哈哈哈

好书

没有什么特别的神秘的技巧，其实就是电路工艺的问题。其实无论是电子还是机械都是工艺的问题，没有工艺不谈什么创新。

绝对实在的入门级大部头读物

这本书技术含量很高，几乎涵盖了数字设计，值得仔细读。

[CMOS超大规模集成电路设计_下载链接1](#)

书评

作者是Mark Horowitz组里曾经的天才学生，年纪轻轻就和Ivan Sutherland一起完成了Logical Effort的完备理论建立，在Domino Circuit Design以及VLSI教学方面是当仁不让的明星。
此书是众多VLSI书中最新最贴近design的一本，虽然它并不叫A Design Perspective。是本用来准备工作...

从大四学长淘到的，用以拓宽视野。内容不用说了，绝对饕餮大餐。很幸运的是，就我读的第一章来看，翻译得还不错。唯一遗憾的是，参考文献没了。怎么，为了省点纸钱？突然觉得和这出版社有代沟了。这种事好像某出版社也干过，不足为奇。

高价求购该绝版书 不管新旧 愿意转让的请联系:QQ332342368
价钱不是问题。高价求购该绝版书 不管新旧 愿意转让的请联系:QQ332342368
价钱不是问题。高价求购该绝版书 不管新旧 愿意转让的请联系:QQ332342368
价钱不是问题。

022912: Still in Chap 1. I like his clear statement on everything and his flow is very smooth. Especially the top level to cell unit make me think about the . For the MIPS architecture, I use his another book:

[CMOS超大规模集成电路设计_下载链接1](#)