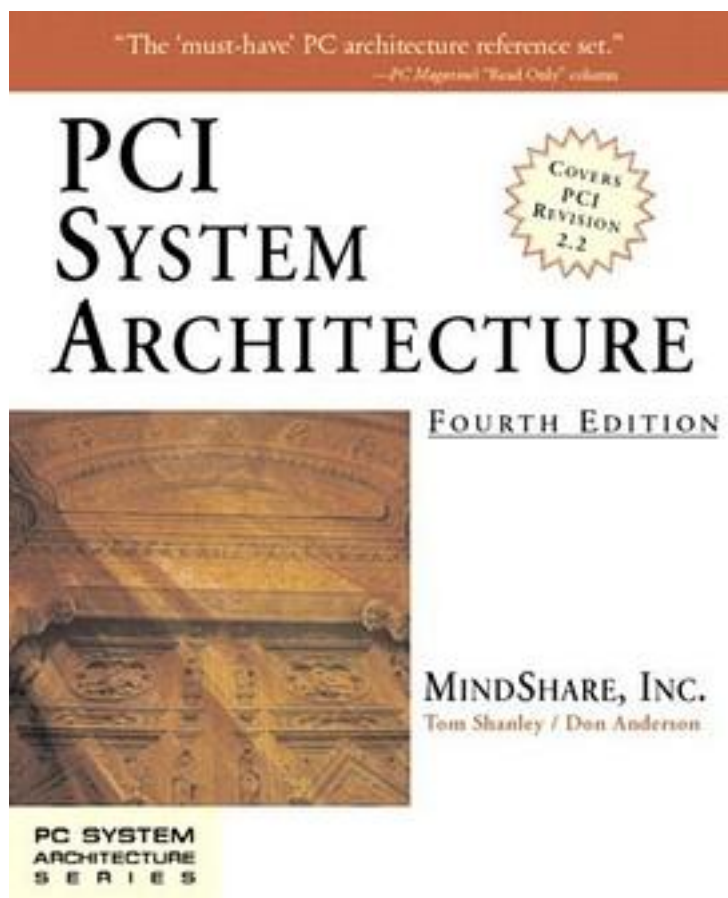


PCI System Architecture (4th Edition)



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PCI System Architecture is a detailed and comprehensive guide to the Peripheral Component Interconnect (PCI) Bus Specification, Intel's technology for fast communication between peripheral devices and the computer processor. This new edition has been thoroughly updated, reorganized, and expanded to cover the PCI

Local Bus Specification version 2.2 and other recent developments, including the new PCI Hot-Plug Specification, changes to the PCI-to-PCI Bridge Architecture Specification, revisions to the PCI Bus Power Management Interface Specification, and the new features of the PCI BIOS Specification. This book provides clear and concise explanations of the relationship of PCI to the rest of the system and PCI fundamentals, including commands, read and write transfers, memory and I/O addressing, error handling, interrupts, and configuration transactions and registers. In addition, you will find specific information on such key topics as: *Hot-Plug Specification *Power management *CompactPCI *The 64-bit PCI Extension *66 MHz PCI Implementation *Expansion ROMs *PCI-to-PCI Bridge and the PCI BIOS *Add-in cards and connectors *Bus arbitration *Reflected-wave switching *Early transaction end *Fast back-to-back and stepping Changes from PCI 2.1 to PCI 2.2 and changes from PCI-to-PCI Bridge Specification 1.0 to 1.1 are visibly highlighted throughout the book so that those familiar with the previous versions can quickly get a handle on new features and functions. Anyone who designs or tests hardware or software involving the PCI bus will find PCI System Architecture, Fourth Edition a valuable resource for understanding and working with this important technology. The PC System Architecture Series is a crisply written and comprehensive set of guides to the most important PC hardware standards. Each title explains from a programmers perspective the architecture, features, and operations of systems built using one particular type of chip or hardware specification.

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