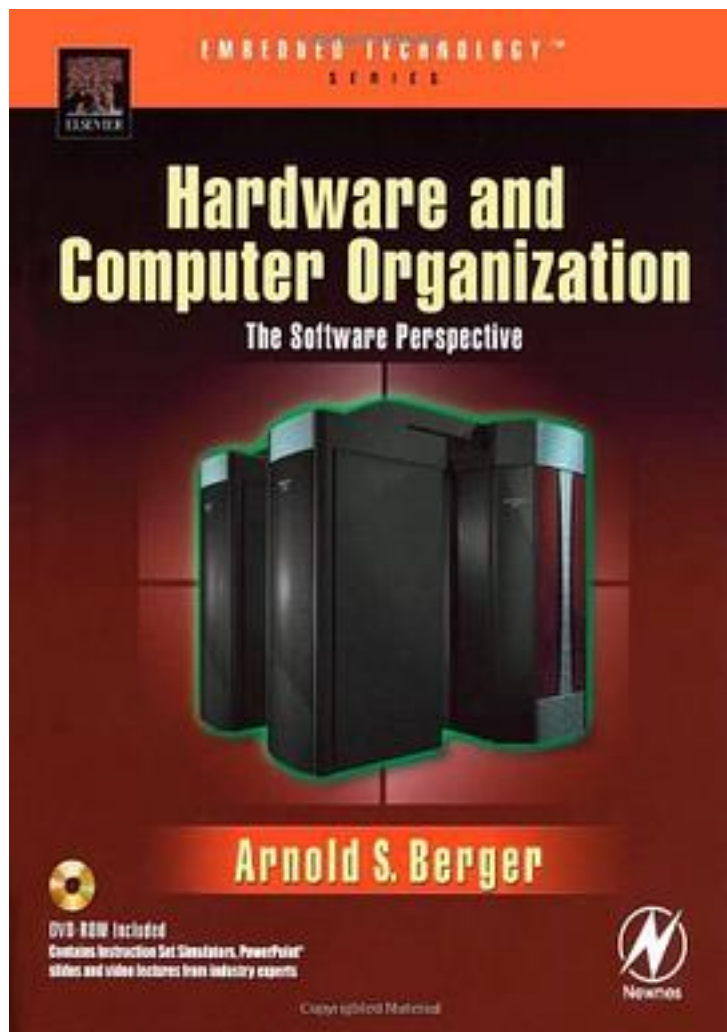


# Hardware and Computer Organization



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"Hardware and Computer Organization" is a practical introduction to the architecture

of modern microprocessors for students and professional alike. It is designed to take practicing professionals 'under the hood' of a PC and provide them with an understanding of the basics of the complex machine that has become such a pervasive part of our everyday life. It clearly explains how hardware and software cooperatively interact to accomplish real-world tasks. Instead of simply demonstrating how to design a computer's hardware, it provides an understanding of the total machine, highlighting strengths and weaknesses, explaining how to deal with memory and how to write efficient assemble code that interacts with and takes best advantage of the underlying hardware. Additionally, the book has a unique emphasis on facilitating the ability to make real engineering decisions in industry when working with anything from simple 8-bit microprocessors in embedded applications to PCs and workstations. It demystifies the link between the behaviour of the code and the operation of the machine, while helping engineers and students to better understand the limitations imposed by finite speed and resources. This book is divided into three major sections: Part 1 - Hardware Fundamentals and Digital Design; Part 2 - Assembly Language Programming; and, Part 3 - Computer Architecture. The DVD accompanying the text will contain the following: source code files for all the code examples used in the text; working demo versions of two different processor simulators; and, video lectures from industry notables covering several of the major topics dealt with in the text.

作者介绍:

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是华盛顿-波泰尔 (Washington-Bothell) 大学计算和软件系统系的高级讲师，拥有康奈尔大学的学士和博士学位。Berger博士曾担任Applied Microsystems公司研发部门的主管、Advanced Micro Devices公司嵌入式工具的营销经理和惠普公司的研发项目经理。Berger博士已发表了40多篇关于嵌入式系统的论文，持有三项专利，并且是畅销书《Embedded Systems Design: An Introduction to Processes, Tools and Techniques》的作者。

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