Computer Arithmetic

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Ideal for graduate and senior undergraduate level courses in computer arithmetic and advanced digital design, Computer Arithmetic: Algorithms and Hardware Designs provides a balanced, comprehensive treatment of computer arithmetic, covering topics in arithmetic unit design and circuit implementation that complement the architectural and algorithmic speedup techniques used in high-performance computer architecture and parallel processing. Using a unified and consistent framework, the text begins with number representation and proceeds through basic arithmetic operations, floating-point arithmetic, and function evaluation methods. Later chapters cover broad design and implementation topics--including techniques for high-throughput, low-power, and fault-tolerant arithmetic--and also feature brief case studies. An indispensable resource for instruction, professional development, and research in digital computer arithmetic, Computer Arithmetic: Algorithms and Hardware Designs combines broad coverage of the underlying theories of computer arithmetic with numerous examples of practical designs, worked-out examples, and a large collection of meaningful problems. Features: DT Divided into 28 lecture-size chapters DT Emphasizes both the underlying theories of computer arithmetic and actual hardware designs DT Carefully links computer arithmetic to other subfields of computer engineering DT Includes over 450 end-of-chapter problems ranging in

complexity from simple exercises to mini-projects DT Incorporates many examples of practical designs DT Uses consistent standardized notation throughout DT Instructor's manual includes solutions to text problems, additional exercises, test questions, and enlarged versions of figures and charts

作者介绍:

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