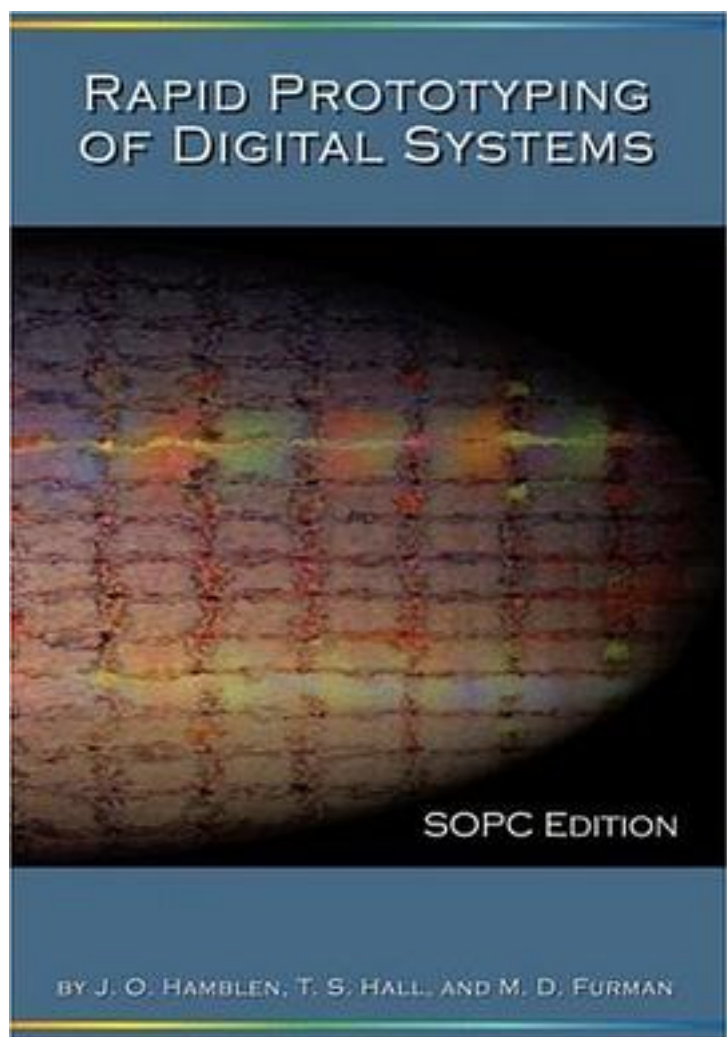


Rapid Prototyping of Digital Systems SOPC Edition



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New to this edition is an introduction to embedded operating systems for SOPC

designs. The μ Clinux OS is configured for Altera's DE1 and DE2 boards and is provided on a DVD along with several application programs.</P>

Featuring four accelerated tutorials on the Quartus II and Nios II design environments, this edition progresses from introductory programmable logic to full-scale SOPC design integrating hardware implementation, software development, operating system support, state-of-the-art I/O, and IP cores.</P>

This edition features Altera's new 7.1 Quartus II CAD and Nios II SOPC tools and includes projects for Altera's DE1, DE2, UP3, UP2, and UP1 FPGA development boards.</P>

Laboratory projects provided on the book's DVD include complete projects for video graphics and text, mouse and keyboard input, and three separate computer examples including a Nios II "reference" design for the DE2, DE1, and UP3 FPGA boards.</P>

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