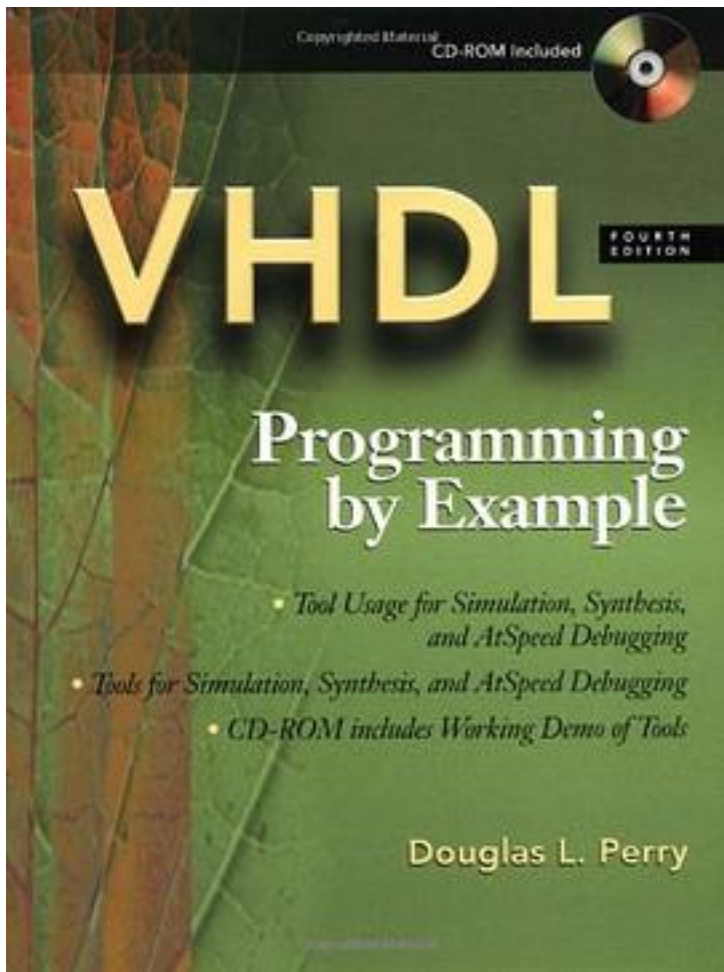


VHDL



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For sophomore/junior-level courses in Digital/Logic and Digital Design Laboratory. For schools that wish to introduce VHDL into their undergraduate computer engineering

sequence, VHDL is a complex language that is worthy of a dedicated course; yet this is not a practical option in most institutions. This companion text enables instructors to integrate the basic concepts of VHDL into existing courses. It is designed to develop an intuition and a structured way of thinking about VHDL models without spending a great deal of time on advanced language features. Yalamanchili gives students a thorough grounding in the basic concepts and language of VHDL, and encourages them to apply what they have learned using realistic examples. Concepts are followed by examples and tutorials.

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