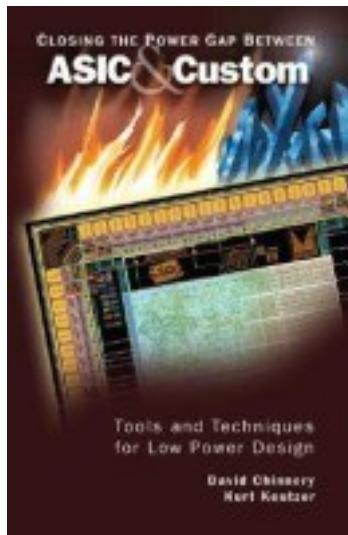


# Closing the Power Gap between ASIC & Custom



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出版者:Springer

出版时间:2007-09-06

装帧:Hardcover

isbn:9780387257631

Explains how to use low power design in an automated design flow, and examine the design time and performance trade-offs Includes the latest tools and techniques for low power design applied in an ASIC design flow Focuses on low power in an automated design methodology, a much neglected area

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