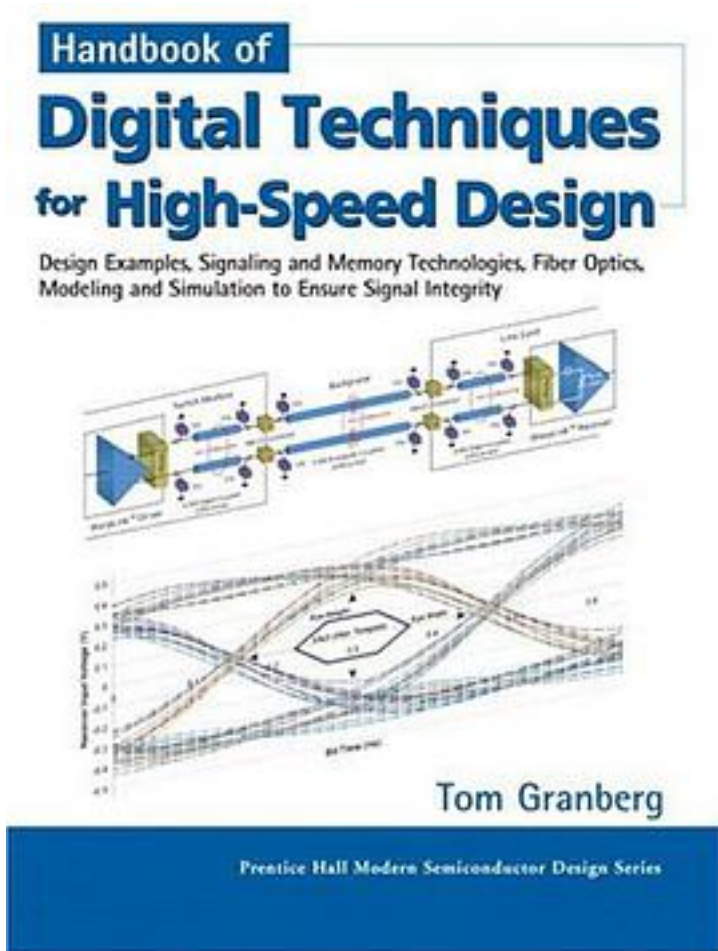# Handbook of Digital Techniques for High-Speed Design

[Handbook of Digital Techniques for High-Speed Design_下载链接1_](#)

著者:Granberg, Tom

出版者:Prentice Hall

出版时间:2004-5

装帧:HRD

isbn:9780131422919

This practical handbook fills in gaps that other textbooks on high-speed design don't

discuss, covering every aspect of high-speed board-level digital design. Several design examples at high Gigabit per second data rates are presented. Discusses highest-speed logic and interface families of devices, relevant applications, and device speeds versus how far signals transmit with good signal integrity. A quick-reference overview of each device family is also provided. High-speed design rules are presented for both engineering design and printed circuit board layout. Emphasizes designing high-speed backplanes, driving cabling, bus architecture and topology. Discusses IBIS and SPICE modeling, simulations, design processes, and over 30 design automation tools. Quantifies signal integrity using jitter and bit error rate measurements, eye diagrams, time-domain reflectometry and transmission. Details high-speed transmission line and parasitic effects, cabling, connectors, single-ended/differential terminations, lab test equipment, and intellectual property. Dedicated chapter on fiber optics and when to use.

作者介绍:

目录:

Handbook of Digital Techniques for High-Speed Design_下载链接1_

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