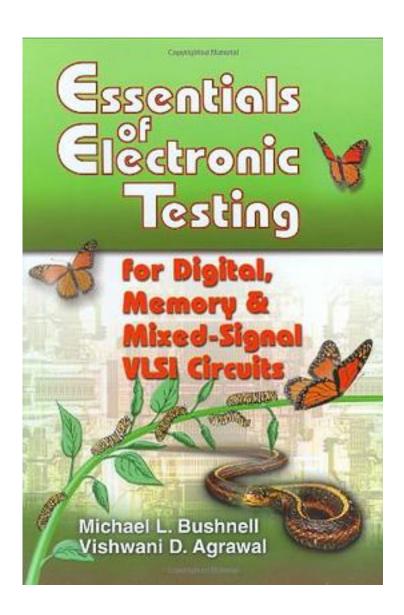
Essentials of Electronic Testing for Digital, Memory and Mixed-signal VLSI Circuits



Essentials of Electronic Testing for Digital, Memory and Mixed-signal VLSI Circuits_下载链接1_

著者:Bushnell, Michael L./ Agrawal, Vishwani D.

出版者:Kluwer Academic Pub

出版时间:2000-11

装帧:HRD

Today's electronic design and test engineers deal with several types of subsystems, namely, digital, memory, and mixed-signal, each requiring different test and design for testability methods. This book provides a careful selection of essential topics on all three types of circuits. The outcome of testing is product quality, which means "meeting the user's needs at a minimum cost". The book includes test economics and techniques for determining the defect level of VLSI chips. Besides being a textbook for a course on testing, it is a complete testability guide for an engineer working on any kind of electronic device or system or a system-on-a-chip.

techniques for determining the defect level of VLSI chips. Besides being a a course on testing, it is a complete testability guide for an engineer work kind of electronic device or system or a system-on-a-chip.
作者介绍:
目录:
Essentials of Electronic Testing for Digital, Memory and Mixed-signal VLSI Circuits 下载链接1_
标签
11
评论
VLSI领域的好教材,美国研究生参考教材
Essentials of Electronic Testing for Digital, Memory and Mixed-signal VLSI Circuits_下载链接1_

书评

Essentials of Electronic Testing for Digital, Memory and Mixed-signal VLSI Circuits_下载链接1_