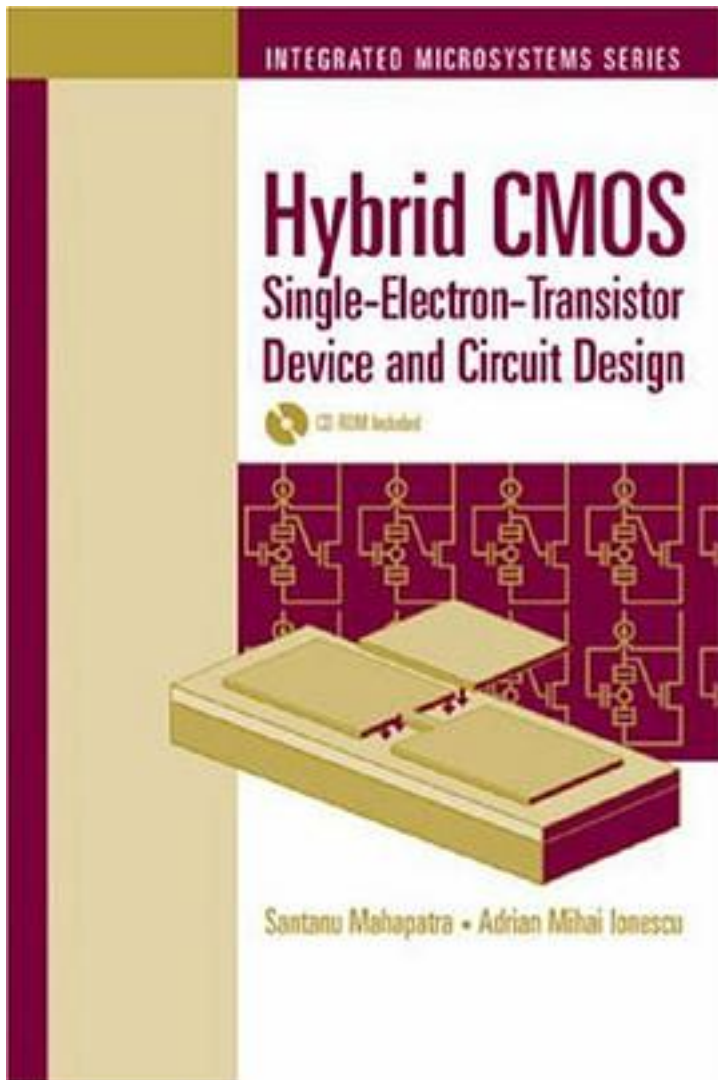


Hybrid CMOS Single-Electron-Transistor Device and Circuit Design



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CMOS (complementary metal oxide semiconductor) is a widely accepted and utilized technology among electrical engineers involved with circuit design. SET (single electron transistor) technology has recently gained significant attention, because it can be combined with CMOSs to improve overall performance of integrated circuits -- reducing power consumption and achieving greater functionality. This cutting-edge resource provides professionals with the conceptual framework and specific strategies for CMOS-SET hybrid circuit design. The book offers a thorough understanding of the pros and cons of digital SETs, explains how SETs can solve the intrinsic drawbacks of CMOS technology, and shows how to incorporate SET technology into a wide variety of design projects. CD-ROM Included Contains valuable tools, such as Matlab code and Verilog-A language, that help users co-simulate and co-design hybrid CMOS-SET circuits.

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