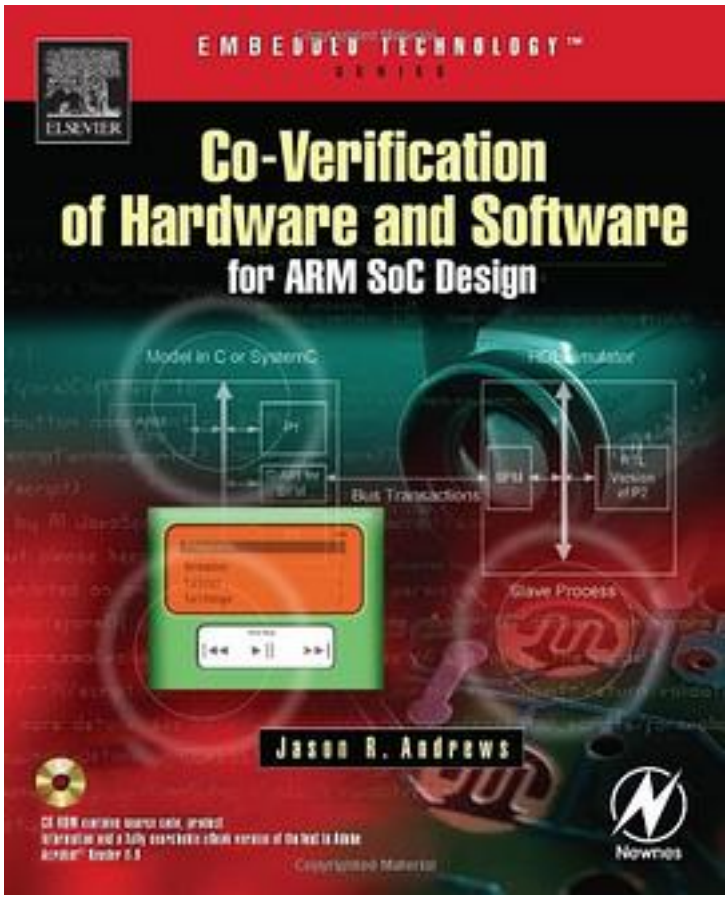
# Co-verification of Hardware and Software for ARM SoC Design

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Hardware/software co-verification is how to make sure that embedded system software works correctly with the hardware, and that the hardware has been properly designed to run the software successfully -before large sums are spent on prototypes

or manufacturing. This is the first book to apply this verification technique to the rapidly growing field of embedded systems-on-a-chip(SoC). As traditional embedded system design evolves into single-chip design, embedded engineers must be armed with the necessary information to make educated decisions about which tools and methodology to deploy. SoC verification requires a mix of expertise from the disciplines of microprocessor and computer architecture, logic design and simulation, and C and Assembly language embedded software. Until now, the relevant information on how it all fits together has not been available. Andrews, a recognized expert, provides in-depth information about how co-verification really works, how to be successful using it, and pitfalls to avoid. He illustrates these concepts using concrete examples with the ARM core - a technology that has the dominant market share in embedded system product design. The companion CD-ROM contains all source code used in the design examples, a searchable e-book version, and useful design tools. It is the only book on verification for systems-on-a-chip (SoC) on the market. It will save engineers and their companies time and money by showing them how to speed up the testing process, while still avoiding costly mistakes. Design examples use the ARM core, the dominant technology in SoC, and all the source code is included on the accompanying CD-ROM, so engineers can easily use it in their own designs.

作者介绍:

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## 标签

SOC

ARM

## 评论

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# 书评

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