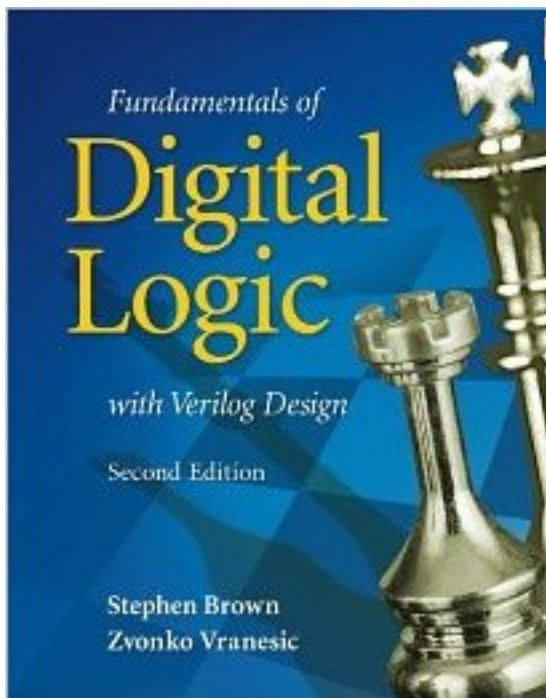# Fundamentals of Digital Logic with Verilog Design

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Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples. Use of CAD software is well integrated into the book. A CD-ROM that contains Altera's Quartus CAD software comes free with every copy of the text. The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). Students will be able to try, firsthand, the book's Verilog examples (over 140) and homework problems. Engineers use Quartus CAD for designing, simulating, testing and implementing logic circuits. The version included with this text supports all major

features of the commercial product and comes with a compiler for the IEEE standard Verilog language. Students will be able to: enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices (using the school's laboratory facilities) Verilog is a complex language, so it is introduced gradually in the book. Each Verilog feature is presented as it becomes pertinent for the circuits being discussed. To teach the student to use the Quartus CAD, the book includes three tutorials.

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标签

计算机

verilog

评论

知识讲的虽然不错但是难度太低 跟lab和test project里面的难度比这书简单就是useless

贵死了，然后还是没学好

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书评

我是顺着译者夏宇闻顺藤摸瓜才发现这本书的。不得不说，这是一本相当有含量的书，书里涉及的知识面很广，但却联系紧密。看这种书就像探宝，循序渐进，可深可浅。而国内多数书籍基本上就是一个知识点汇总而已，读起来索然无味。这本书可真是狠狠地甩了那些刻板教科书一个耳光了...

本书辅以Verilog
HDL讲解数字逻辑设计，知识点与新技术同步，讲解的十分透彻，是哪种能把复杂问题说简单的好书。关键是翻译的也非常细致，难得！感谢作者和译者的辛勤劳动，使我用较少的时间获得大量的新鲜的知识。

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