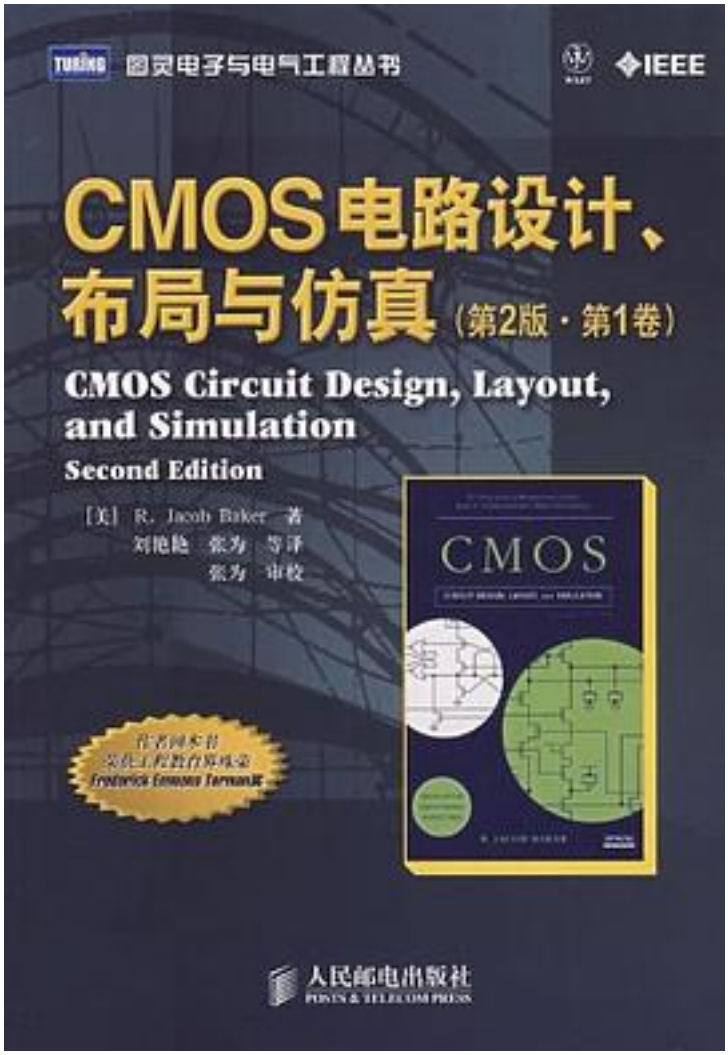
CMOS 电路设计、布局与仿真



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《CMOS
电路设计、布局与仿真(第2版·第1卷)》是CMOS集成电路设计领域的一部力作，是作者20多年教学和研究成果的总结，内容涵盖电路设计流程、EDA软件、工艺集成、器件、模型、数字和模拟集成电路设计等诸多方面，由基础到前沿，由浅入深,结构合理，特色鲜明。

作者介绍:

Russel Jacob (Jake) Baker (S’83-M’88-SM’97) was born in Ogden, Utah, on October 5, 1964. He received the B.S. and M.S. degrees in electrical engineering from the University of Nevada, Las Vegas, in 1986 and 1988. He received the Ph.D. degree in electrical engineering from the University of Nevada, Reno in 1993.

From 1981 to 1987, he served in the United States Marine Corps Reserves. From 1985 to 1993, he worked for E. G. & G. Energy Measurements and the Lawrence Livermore National Laboratory designing nuclear diagnostic instrumentation for underground nuclear weapons tests at the Nevada test site. During this time he designed over 30 electronic and electro-optic instruments including high-speed (750 Mb/s) fiber-optic receiver/transmitters, PLLs, frame- and bit-syncs, data converters, streak-camera sweep circuits, Pockell’s cell drivers, micro-channel plate gating circuits, and analog oscilloscope electronics. From 1993 to 2000, he served on the faculty in the department of electrical engineering at the University of Idaho on the Boise State campus. In 2000, he joined a new electrical and computer engineering program at Boise State University, where he served as department chair from 2004 to 2007. At Boise State he helped establish graduate programs in electrical and computer engineering including, in 2006, the university’s second PhD degree. Also, since 1993, he has consulted for various companies and laboratories including: Aerius Photonics, Arete’ Associates, Amkor, Contour Semiconductor, the Lawrence Berkeley Laboratory, Micron, Nascentric, Oracle, Rendition, Sun, and Tower. His research interests lie in analog/mixed-signal integrated circuit design (combining analog circuit design with digital signal processing) and the design of memory/displays/imagers (arrays) in new and emerging fabrication technologies.

Jake holds over 200 granted or pending patents in integrated circuit design. Among his inventions is the K-Delta-1-Sigma modulator topology used in the Baker analog-to-digital converter. He is a member of the electrical engineering honor society Eta Kappa Nu, a licensed Professional Engineer, and the author of the books CMOS Circuit Design, Layout, and Simulation, CMOS Mixed-Signal Circuit Design, and a coauthor of DRAM Circuit Design: Fundamental and High-Speed Topics. He received the 2000 Best Paper Award from the IEEE Power Electronics Society, the 2007 Frederick Emmons Terman Award, and the 2011 IEEE Circuits and Systems (CAS) Education Award. Jake currently serves on the IEEE Solid-State Circuits Society Administrative Committee (AdCom) and as editor for the Wiley-IEEE Press book Series on Microelectronic Systems.

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评论

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【有一种感觉知道这本书翻译得很糟糕非常有用。。。】大头老师啊！好老师！嘿，各位，对不起，又算错了嘿嘿嘿嘿嘿。。好可爱！大学最后一门课。。。。桑感。。。。。大学没有善始不过好在善终了~

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