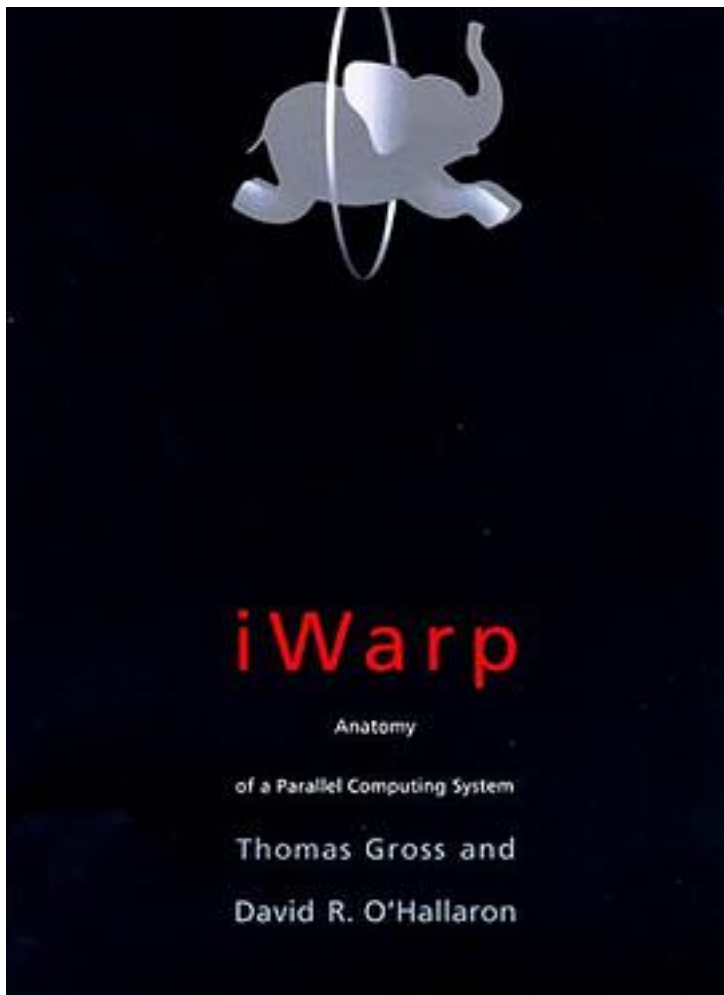


iWARP



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Although researchers have proposed many mechanisms and theories for parallel systems, only a few have actually resulted in working computing platforms. The iWarp

is an experimental parallel system that was designed and built jointly by Carnegie Mellon University and Intel Corporation. The system is based on the idea of integrating a VLIW processor and a sophisticated fine-grained communication system on a single chip. This book describes the complete iWarp system, from instruction-level parallelism to final parallel applications. The authors presents a range of issues that must be considered to get a real system into practice. They also provide a start-to-finish history of the project, including what was done right and what was done wrong, that will be of interest to anyone who studies or builds computer systems.

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