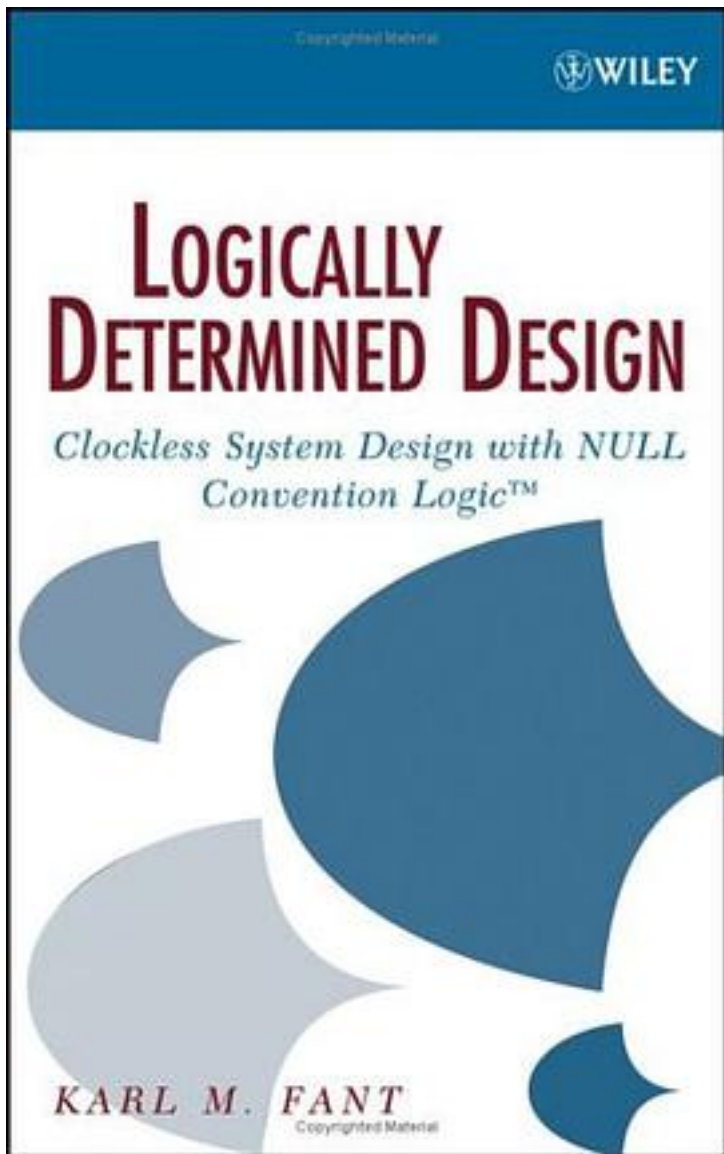


Logically Determined Design



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This seminal book presents a new logically determined design methodology for designing clockless circuit systems. The book presents the foundations, architectures and methodologies to implement such systems. Based on logical relationships, it concentrates on digital circuit system complexity and productivity to allow for more reliable, faster and cheaper products. aeo Transcends shortcomings of Boolean logic. aeo Presents theoritical foundations, architecture and analysis of clockless (asynchronous) circuit design. aeo Contains examples and exercises making it ideal for those studying the area.

作者介绍:

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