#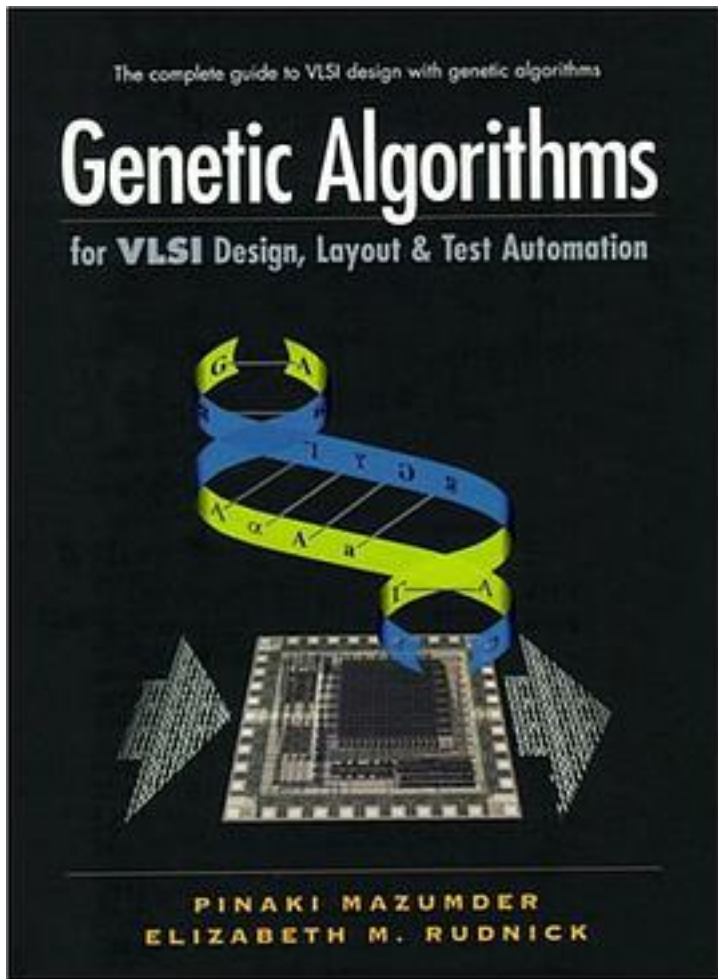 Genetic Algorithms for VLSI Design, Layout and Test Automation (Prentice Hall Modern Semiconductor Design Series' Sub Series

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This book provides details about some of the EDA applications where GAs have been used. These applications include partitioning, automatic placement and routing, technology mapping for FPGAs, automatic test generation, and power estimation. One chapter is devoted to each of these topics. The objective is to provide examples where GAs have been successfull applied in the past so that the reader will be able to apply similar techniques in solving his/her own problems.

作者介绍:

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