

Digital Electronics Laboratory Experiments Using the Xilinx XC95108 CPLD with Xilinx Foundation



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For freshman/sophomore-level courses in Digital Circuit Design, Digital System Design, and Computer Engineering Technology. This manual offers an easy-to-read, easy-to-follow approach to digital fundamentals through the use of Complex Programmable Logic Devices (CPLDs). The first shorter section of the book contains a set of lab jobs using a single TTL chip: the 74LS00 quad 2-input NAND gate, allowing students to build a few simple circuits immediately. The second section contains a set of hands-on lab jobs with step-by-step instructions on using the Xilinx XC95108 CPLD. The use of advanced logic device technology in an introductory digital course prepares students both for lab work in advanced courses as well as for using an industry-standard design environment.

作者介绍:

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