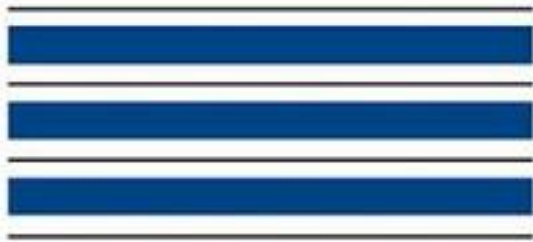


Integrating Functional and Temporal Domains in Logic Design

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**Patrick C. McGeer
Robert K. Brayton**



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