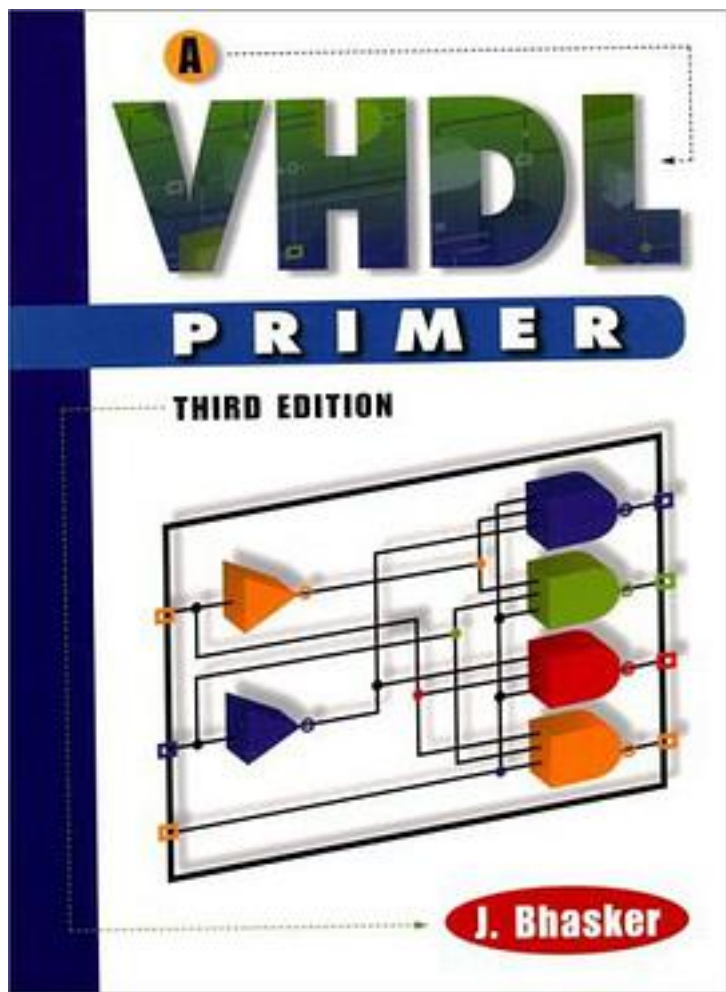


# A VHDL Primer



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The aim of this book continues to be the introduction of the VHDL language to the reader at the beginner's level. VHDL is a large and verbose language with many

complex constructs that have complex semantic meanings and is initially difficult to understand (the US military requires VHDL for device designs, thus explains its popularity vs. other HDLs). The book presents a subset of VHDL consisting of commonly used features that make it both simple and easy to use.

作者介绍:

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