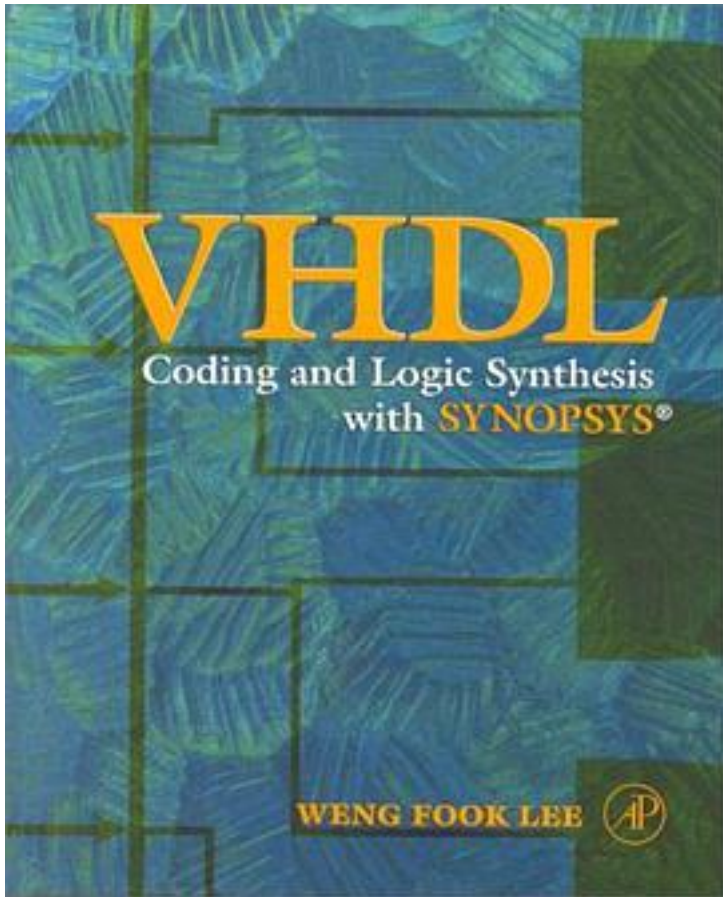


VHDL Coding and Logic Synthesis with Synopsys



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著者:Weng Fook Lee

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