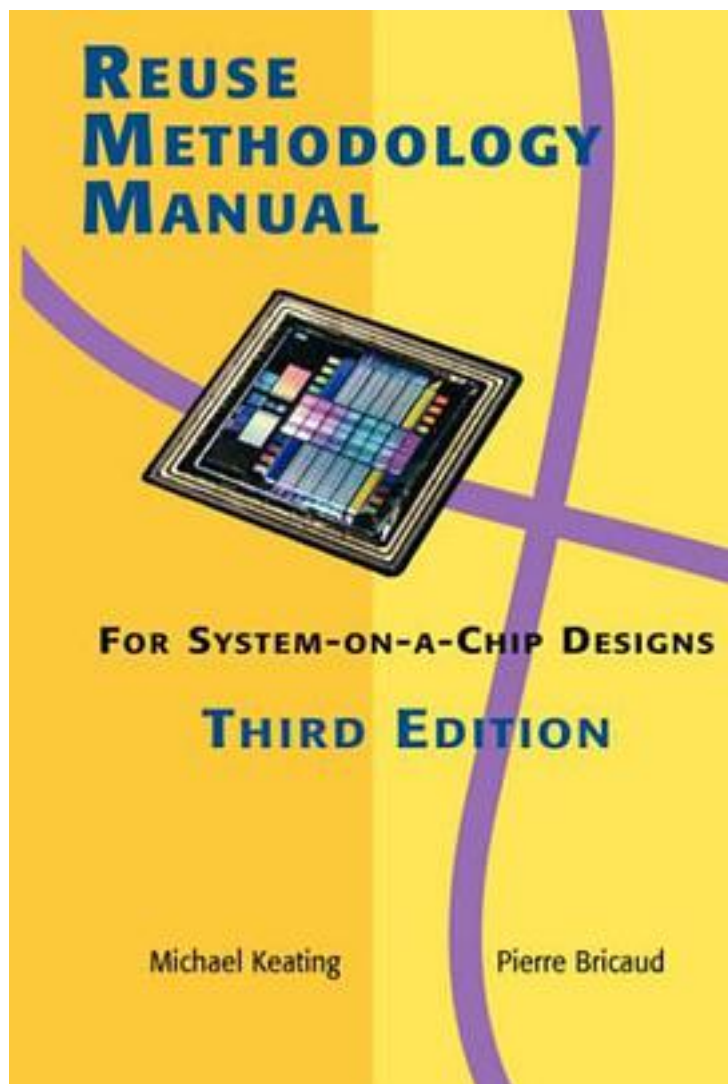


Reuse Methodology Manual for System-on-a-chip Designs



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This revised and updated third edition outlines a set of best practices for creating reusable designs for use in an System-on-a-Chip (SoC) design methodology. These practices are based on the authors' experience in developing reusable designs, as well as the experience of design teams in many companies around the world.

作者介绍:

目录:

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