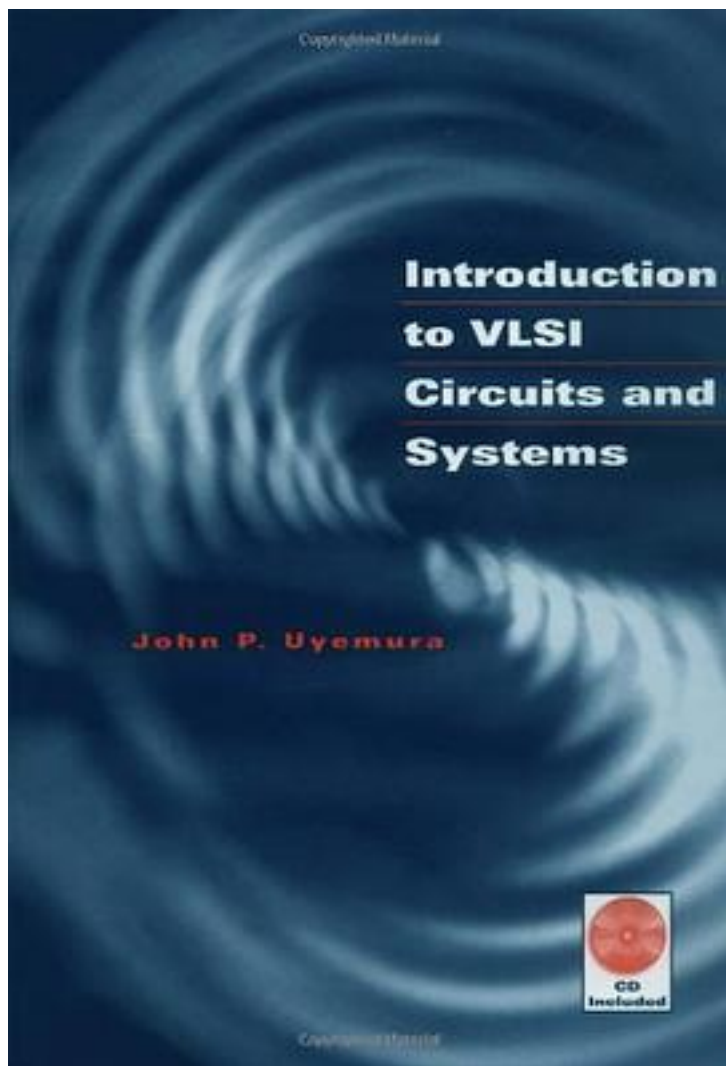


Introduction to VLSI Circuits and Systems



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著者:John P. Uyemura

出版者:Wiley

出版时间:2001-6-27

装帧:Hardcover

isbn:9780471127048

Presents modern CMOS logic circuits, fabrication, and layout in a cohesive manner that links the material together with the system-level considerations.

- * Chapter on Verilog HDL allows for rapid start-up.

- * Illustrates the top-down design procedure used in modern VLSI chip design with an emphasis on variations in the HDL, logic, circuits and layout.

作者介绍:

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