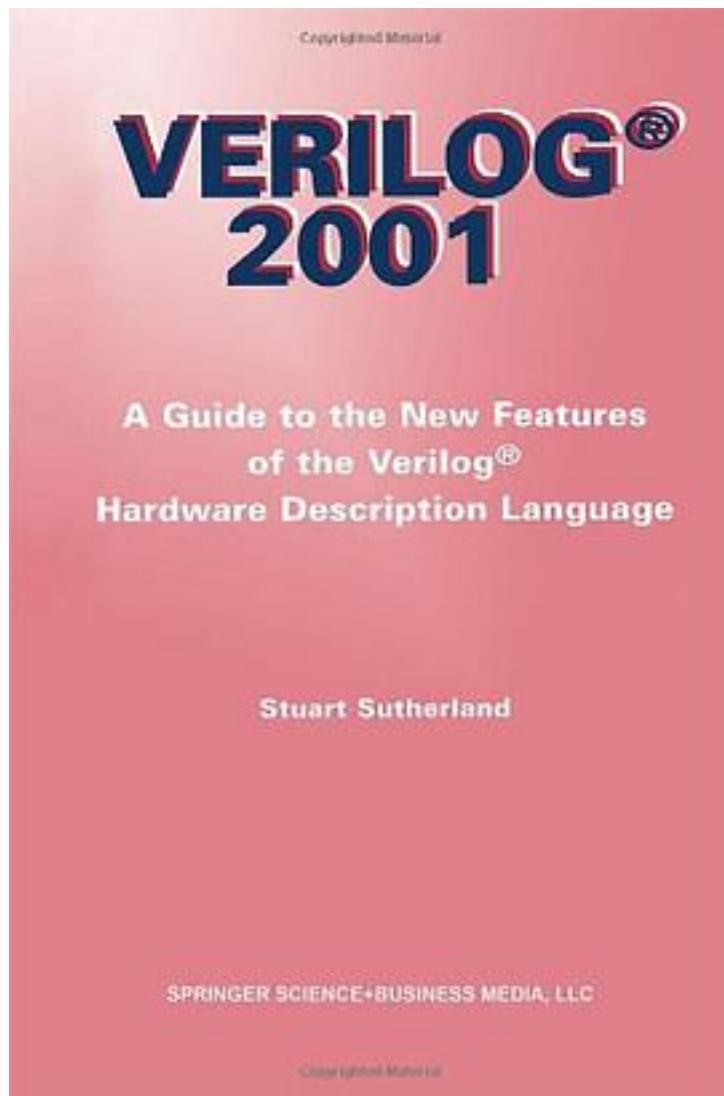


Verilog-2001



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The IEEE 1364-2001 standard, nicknamed 'Verilog-2001', is the first major update to the Verilog language since its inception in 1984. This book presents 45 significant enhancements contained in Verilog-2001 standard. A few of the new features described in this book are: * ANSI C style port declarations for modules, primitives, tasks and functions; * Automatic tasks and functions (re-entrant tasks and recursive functions); * Multidimensional arrays of any data type, plus array bit and part selects; * Signed arithmetic extensions, including signed data types and sign casting; * Enhanced file I/O capabilities, such as \$fscanf, \$fread and much more; * Enhanced deep submicron timing accuracy and glitch detection; * Generate blocks for creating multiple instances of modules and procedures; * Configurations for true source file management within the Verilog language. This book assumes that the reader is already familiar with using Verilog. It supplements other excellent books on how to use the Verilog language, such as *The Verilog Hardware Description Language*, by Donald Thomas and Philip Moorby (Kluwer Academic Publishers, ISBN: 0-7923-8166-1) and *Verilog Quickstart: A Practical Guide to Simulation and Synthesis*, by James Lee (Kluwer Academic Publishers, ISBN: 0-7923-8515-2).

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