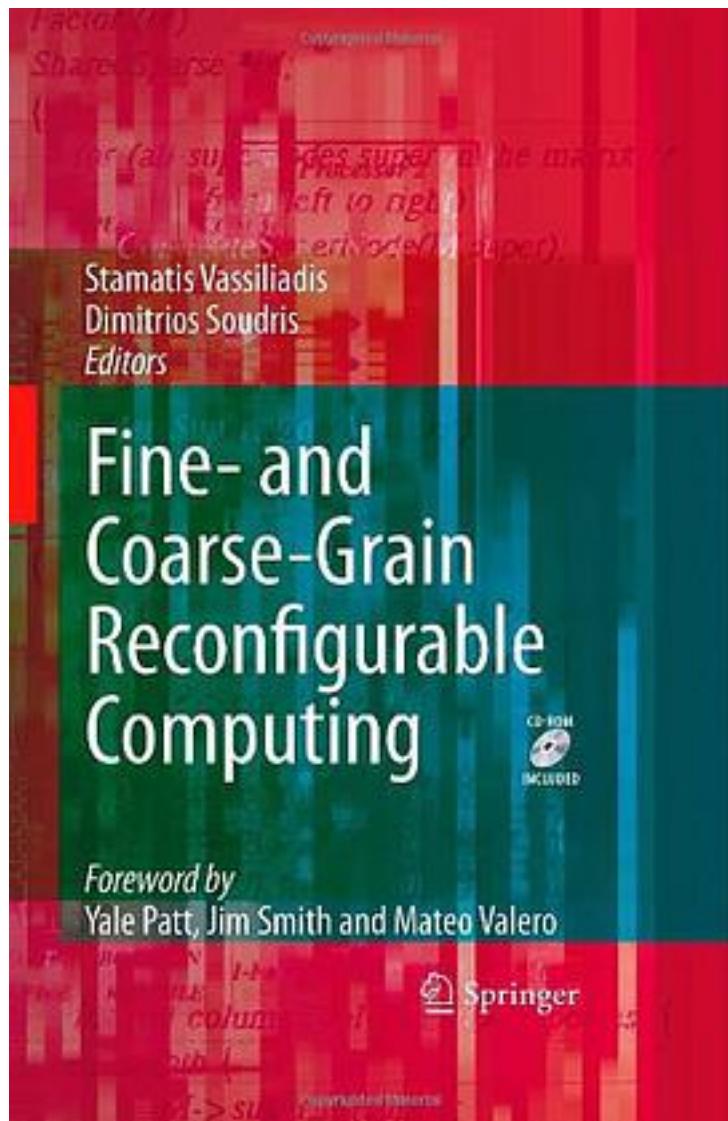


Fine- and Coarse-grain Reconfigurable Computing



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著者:Vassiliadis, Stamatis (EDT)/ Soudris, Dimitrios (EDT)/ Patt, Yale (FRW)/ Smith, Jim (FRW)/ Valero, Mateo (FRW)

出版者:

出版时间:2007-10

装帧:

isbn:9781402065040

The basic concepts and building blocks for the design of Fine- (or FPGA) and Coarse-Grain Reconfigurable Architectures are discussed in this book. Recently-developed integrated architecture design and software-supported design flow of FPGA and coarse-grain reconfigurable architecture are also described.

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