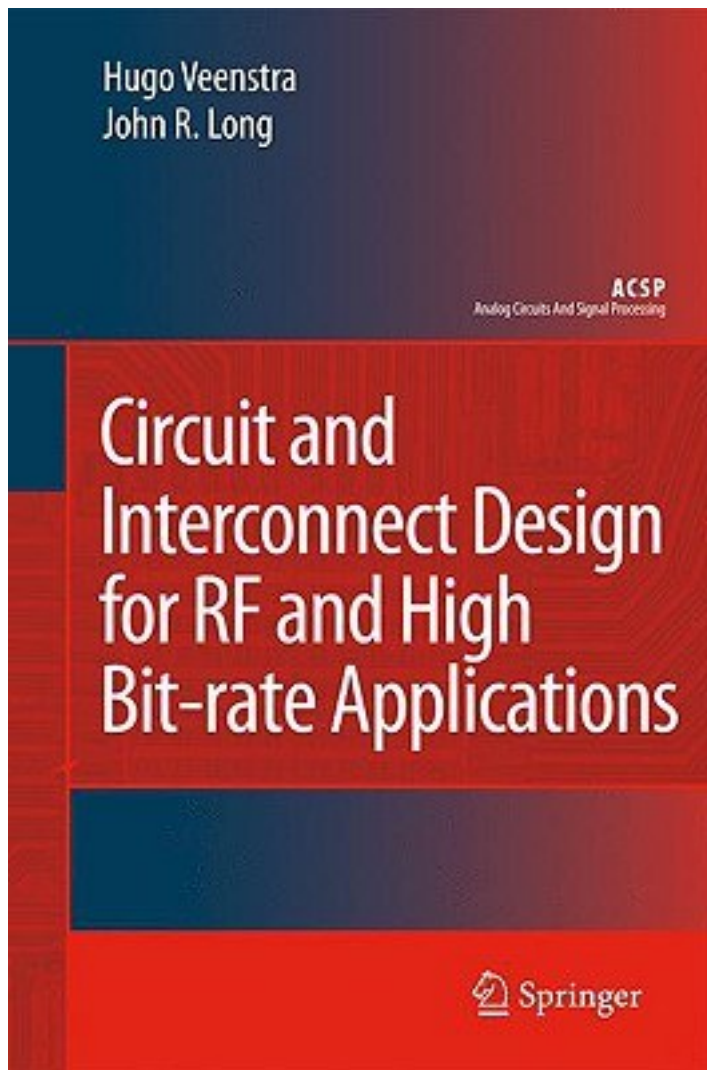


Circuit and Interconnect Design for RF High Bit-rate Applications



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