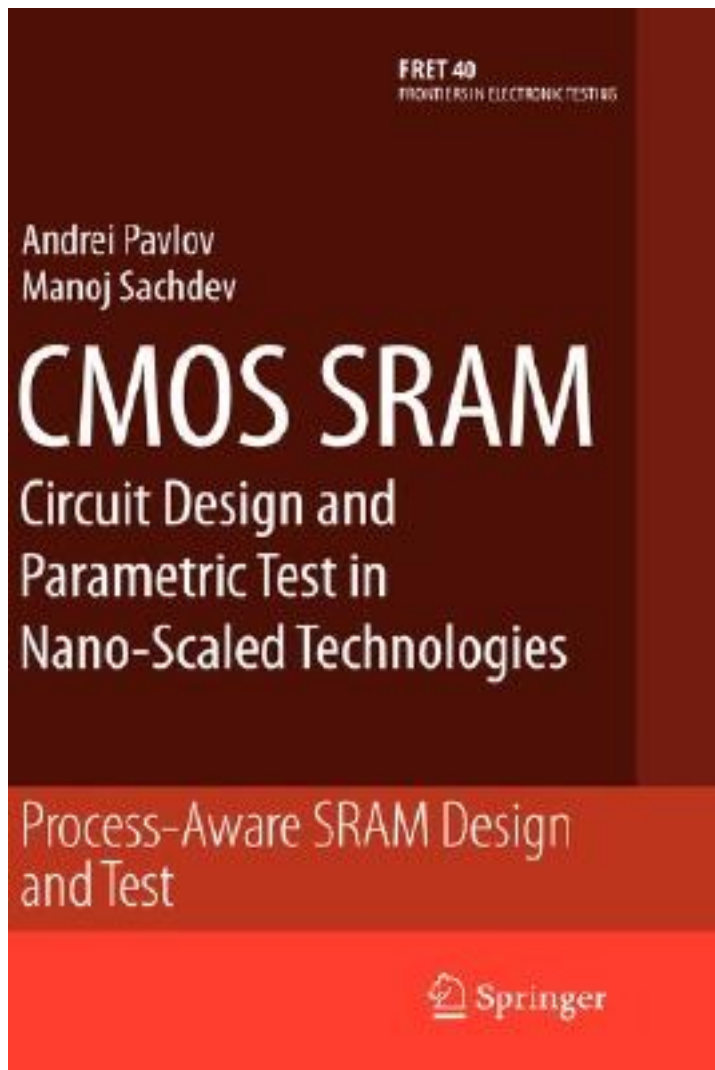


CMOS SRAM Circuit Design and Parametric Test in Nano-Scaled Technologies



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