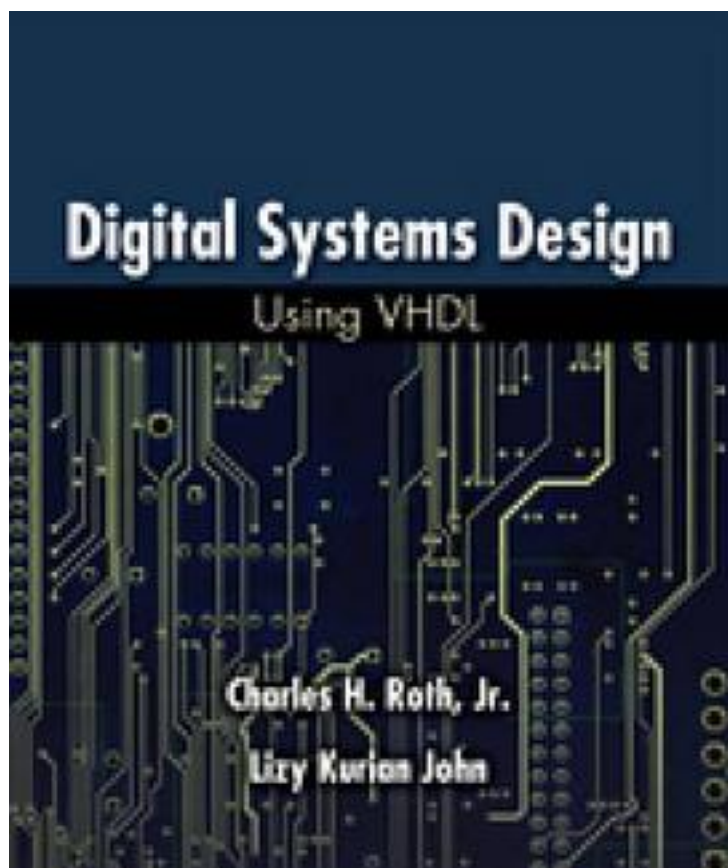# Digital Systems Design Using VHDL



[Digital Systems Design Using VHDL_下载链接1_](#)

著者:Roth, Charles H., Jr.

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Written for an advanced-level course in digital systems design, DIGITAL SYSTEMS DESIGN USING VHDL integrates the use of the industry-standard hardware description language VHDL into the digital design process. Following a review of basic concepts of logic design, the author introduces the basics of VHDL, and then incorporates more coverage of advanced VHDL topics. Rather than simply teach VHDL as a programming

language, this book emphasizes the practical use of VHDL in the digital design process.

作者介绍:

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