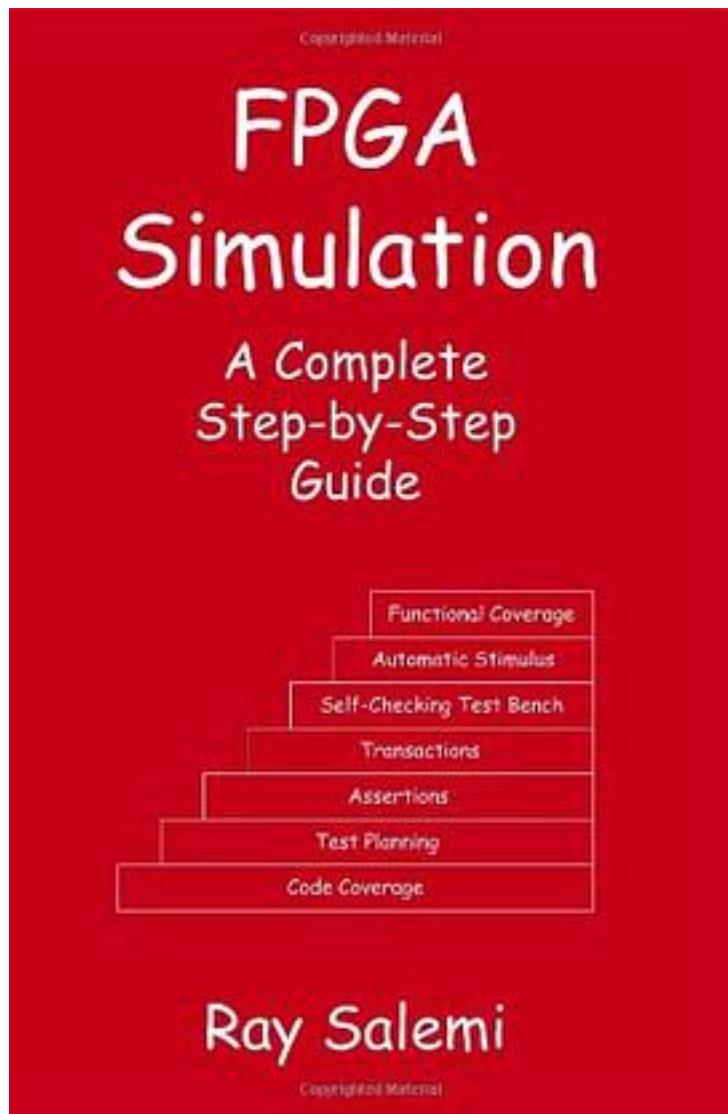


FPGA Simulation



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FPGA Simulation: A Complete Step-by-Step Guide shows FPGA design engineers how to avoid long lab debug sessions by simulating with SystemVerilog. The book helps engineers to have never simulated their designs before by bringing them through seven steps that can be added incrementally to a design flow. Engineers start with code coverage as the first step. Succeeding steps introduce test planning, assertions, and SystemVerilog simulation techniques. By the end of the process engineers who have never simulated before will know how to create complete self-checking test benches that generate their own stimulus, and demonstrate complete functional coverage. This book is a must for engineers who are facing DO-254 certification requirements on their next FPGA project.

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